# **User's Guide**

RIGOL

Publication number UGB03107-1310 July, 2008

# Logic Signal Output Module

© 2008 RIGOL Technologies, Inc. All Rights Reserved.

- © 2008 **RIGOL** Technologies, Inc. All Rights Reserved.
- **RIGOL** products are protected by patent laws in and outside of the P.R. China.
- Information in this publication replaces all previous corresponding material.
- **RIGOL** reserves the right to modify or change part of or all the specifications and pricing policies at company's sole decision.

NOTE: RIGOL is registered trademark of RIGOL Technologies, Inc.

## Safety Notices

Review the following safety precautions carefully before operating the instrument to avoid any personal injury or damage to the instrument or products connected to it.

To avoid potential hazards, use the instrument in a manner only as specified by this user manual.

The instrument should be serviced by qualified personnel only.

### To avoid fire or personal injury

#### \*Use proper power cord.

Only the dedicated power cord of the products approved by the State should be used.

### Connect and disconnect accessories properly.

Do not connect or disconnect probes or test leads while they are connected to a voltage source.

### \*Ground the instrument.

This generator is grounded through the protective earthing conductor of the power cord. To avoid electric shock, the grounding conductor must be connected to earth ground. Maker sure the instrument is properly grounded before connecting the input or output terminals.

#### **Observe all terminal ratings.**

To avoid fire or shock, observe all ratings and marks on the instrument. Follow the user manual for further rating information before making connections to the instrument.

#### Do not operate without covers.

Do not operate your generator with covers or panels removed.

#### \*Use proper fuse.

© 2008 RIGOL Technologies, Inc.

Only use the fuse type and rating specified for this product.

#### Avoid circuit or wire exposure.

Do not touch exposed connections or components when they are on.

#### Do not operate with suspected failures.

If you suspect damage with this product, have it inspected by qualified service personnel before further operations.

#### Provide proper ventilation.

#### Do not operate in wet/damp conditions

#### Do not operate in an explosive atmosphere

#### Keep product surfaces clean and dry

**NOTE:** Items with \* are for the host machine of DG3000 Series Function/Arbitrary Waveform Generator.

### Safety Terms and Symbols

Terms in this manual. These terms may appear in this manual:



**WARNING:** Warning statements identify conditions or practices that could result in injury or loss of life.



**CAUTION:** Caution statements identify conditions or practices that could result in damage to this product or other property.

Terms on the product: These terms may appear on the product:

DANGER indicates an injury hazard that may be immediately accessible.

**WARNING** indicates an injury hazard that may be not immediately accessible.

**CAUTION** indicates that a potential damage to the instrument or other property might occur.

**Symbols on the product:** These symbols may appear on the Instrument:



Hazardous Voltage

Refer to Instructions



Protective Earth Terminal

Grounding Terminal of Chassis

Test Grounding Terminal

# Logic Signal Output Module at a Glance

**RIGOL** company's Logic Signal Output Module which has 16-channel data outputs and 2-channel clock outputs, making DG3000 series practical and meaningful Mixed Signal Generator (MSG). The logic outputs can be configured according to user's needs for specific timing of digital signals. The outputs can be used as logic circuit signal sources, for testing and analysis of the logic circuit designs. General digital signal protocols can be constructed easily; in conjunction with the analog channels, real life mixed signals can be produced.

The Logic Signal Output Module has the following function features:

- Module operation can be turned "ON" or "OFF";
- Two operation modes: "Depend" and "Independ";
- Two trigger modes: Auto, Burst;
- Four protocols: RS232、I<sup>2</sup>C、SPI、PO;
- User-defined protocol output based on PO protocol;
- A variety of code pattern outputs: All '0', All '1', '01' turn, IEEE standard pseudorandom sequence;
- Online data editing of protocols;
- Arbitrary output rate in the "Depend" work mode;
- User-defined channels output, including data and clock lines (choose from 16+2);
- Digital channel and analog channel outputs and respectively setting of channels output or not;
- Provide TTL, LVTTL, CMOS, LVCMOS and user-defined output voltage;
- Remote configuration;
- Embedded help.

#### Expressions in this manual:

The expressions of buttons in this manual are the same as those on the front panel. The operation buttons are surrounded by a textbox, such as the Arb, and texts with shadow, such as Edit Digital W, indicate the operation menu of editing digital waveform in Arb function. The five menu keys at the right side of the LCD are labeled as F1, F2, F3, F4 and F5 from top to bottom.

### NOTE:

The DG3000 models which are interoperable with the Logic Signal Output Module are: DG3121A  $\$  DG3101A and DG3061A.

# Catalog

Safety Notices	II
Logic Signal Output Module at a Glance	V
Chapter 1 Quick Start	1-1
Ports of the Module	1-2
To Connect the Module	1-5
User Interface	1-8
Communication Protocol Interface	1-9
Data Edit Interface	1-11
Chapter 2 To Edit Digital Waveform	2-1
Protocol Setting	2-2
To Set RS232 Protocol	2-3
To Set SPI Protocol	2-5
To Set IIC Protocol	2-7
To Set PO Protocol	2-9
To Set the Code Pattern	2-12
Common Code Pattern	2-12
User Define Code Pattern	2-13
User Data Editor	2-14
Edit Address and Data	2-14
Edit Stored Digital Waveform	2-16
Data Browse, Store and Recall	2-17
Output User Data	2-24
To Set the Output Data Length	2-27
To Set the Data Transmission Rate	2-28
To Set the Trigger Mode	2-31
Channel Setting	2-35
Channel Setting for RS232 Protocol	2-35
Channel Setting for SPI Protocol	2-37
Channel Setting for IIC Protocol	2-39
Channel Setting for PO Protocol	2-41
Digital Waveform Calibration	2-43
Chapter 3 Examples	3-1
Example 1: To Generate a RS232 Digital Waveform	3-2
Example 2: To Generate a SPI Digital Waveform	3-4
Example 3: To Generate an IIC Digital Waveform	
© 2008 RIGOL Technologies, Inc.	VII

Example 4: To Generate a PO Digital Waveform	
Example 5: To Generate an IIC Digital Waveform Using the PO Proto	col 3-10
Chapter 4 Prompt Message & Troubleshooting	4-1
Prompt Message	4-1
Common Prompt Message	
Error Message	
Data Overflow	
Troubleshooting	4-7
Chapter 5 Specifications	5-1
Chapter 6 Appendix	6-1
Appendix A: Accessories	6-1
Appendix B: Warranty	6-2
Appendix C: Maintenance	6-3
Appendix D: Contact RIGOL	6-4
Index	1

# Chapter 1 Quick Start

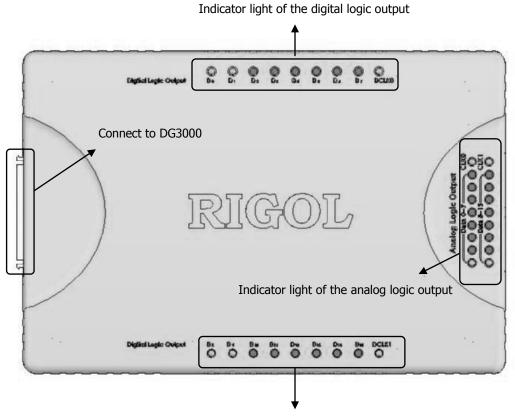
This chapter introduces the connections and user interfaces of the module, and leads users to get familiar how to use the module.

This chapter is divided into the following sections:

- Ports of the module
- To connect the module
- User interface

### **Ports of the Module**

When you get a new DG3000 Logic Signal Output Module, first be familiar with the ports and markings on it. There are three main ports: port connected to DG3000, a digital logic output and an analog logic output, as shown in Figure 1-1.



Indicator light of the digital logic output

Figure 1-1 Port instruction of the module

As shown in Figure 1-2, to use the module, please connect this port to the "DIGITAL OUTPUT" connector at the rear panel of DG3000 with the provided cable as described in "Appendix A".

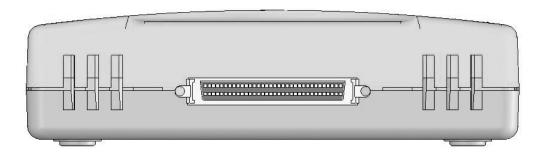


Figure 1-2 Port used for connecting to DG3000

The analog logic output port and the pin definitions are shown in Figure 1-3. To use it, please connect the provided logic analyzer testing wires to this port; connecte the other ends of the testing wires to the input of a logic analyzer which will verify the output signal of the module. The logic analyzer testing wires can also be used to transform the analog logic output.

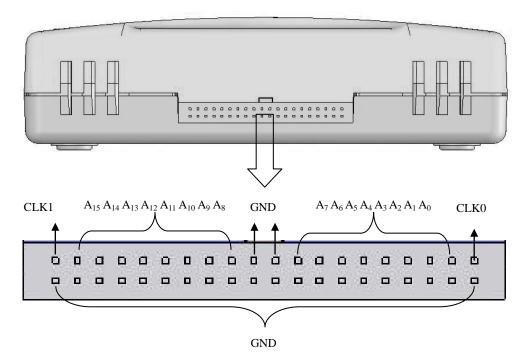


Figure 1-3 Port instruction of the analog logic output

The digital logic output port is shown below in Figure 1-4. The outputs are located at the two sides of the module. The module provides 16 digital channels ( $D_0 \sim D_{15}$ ) + 2 clock output channels (DCLK0, DCLK1).

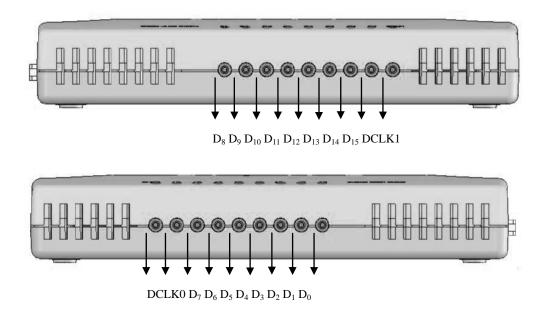


Figure 1-4 Ports instruction of the digital logic output

### **To Connect the Module**

The Logic Signal Output Module is an optional accessory of the DG3000 Series Function/Arbitrary Waveform Generator. Connect the module to a DG3000 as shown in Figure 1-5. Make sure that the power is off before and during the connection. Power up the instrument, the DG3000 will confirm if the module has been successfully installed by displaying the message: "Digital module is installed". Otherwise, repeat the above setup process until the screen shows the success message.

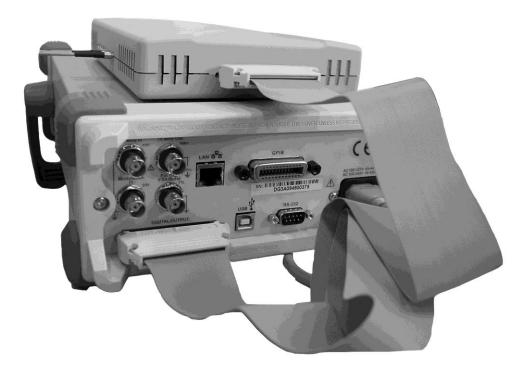


Figure 1-5 Connect the module to DG3000



**WARNING** When connecting the module to a DG3000, the power must be turned off.

DG3000 provides a switch function to control the power modes of the module. Press Utility  $\rightarrow$  Output Setup  $\rightarrow$  Digit-Modu, as shown in Figure 1-6, there are two power states: "Power-On" and "Power-Off".

The module supports two work modes: "Depend" and "Independ".

At the "Depend" mode, all the analog waveforms output are disabled, the output rate and phase of digital output will have not limit.

At the "Independ" mode, both the module and DG3000 can works normally. However, the output rate and phase of module will have "some limit" (See the following prompt message).

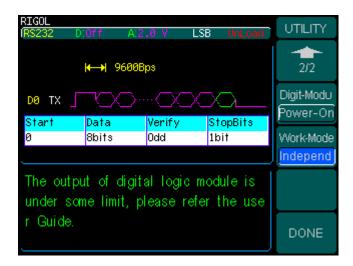


Figure 1-6 Choose "Independ" mode

Examples in this manual use the "Independ" mode to introduce the functions and operation methods of the module, as shown in Figure 1-6 Choose "Independ" mode, the <u>Arb</u> button will blink indicating that the modified settings have not been activated, Press <u>Arb</u> again to execute.

### Limit of the output rate

In "Independ" work mode, the output rate has the following limit:

- RS232: (100MBps/120MBps) / (2×N), (N=1, 2, 3, ...)
- SPI: (100Mbps/120Mbps) / (2×N), (N=1, 2, 3, ...)
- IIC: (100Mbps/120Mbps) / (8×N), (N=1, 2, 3, ...)
- PO: (100Mbps/60Mbps) / N, (N=1, 2, 3, ...)

## **User Interface**

DG3000 Series Function/Arbitrary Waveform Generators provide users clear and easy-to-use graphical displays for: Communication Protocol Interface, and Data Editing Interface.

Communication protocol interface allows users to configure the system:

- Select digital communication protocols
- Select multifarious code patterns or user-defined patterns
- Set the data output length
- Select data transmission rate or user-defined rate output
- Set trigger mode and trigger level
- Configure output channels with various signal line.

Data Editing Interface allows users to:

- Edit data in hexadecimal or binary form
- Store the waveform data in non-volatile memory,
- Recall and edit stored data.

### Communication Protocol Interface

Press Arb  $\rightarrow$  Edit Digital W, to enter the communication protocol interface as shown in Figure 1-7. The upper-left is Status bar, from left to right are: current protocol, digital voltage, analog voltage, data transmission manner and configuration state. Below the Status bar is a Figure shape of the protocol; consists of data transmission rate, configuration of data-line and clock-line; then the Protocol format. Below the Protocol format is the basic Parameter settings. The right side of the screen is the Operation Menus.

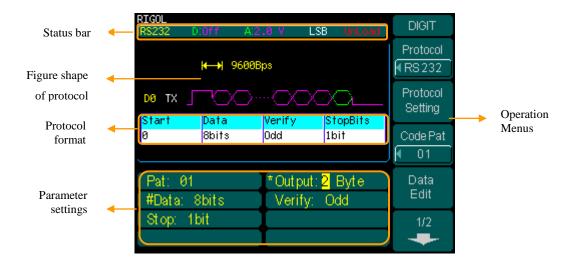


Figure 1-7 Communication protocol interface

### PROMPT

- "Loaded" indicates that the current output is the current configuration, while "Unload" indicates that the current output is the last configuration.
- Parameters marked with "\*" indicate that they have been modified.
- The blinking Arb button indicates that the modified setting has not been implemented, press Arb again to execute.
- Press <u>Arb</u> to complete change of the configuration, or switch to arbitrary wave mode if no parameter is changed.

DIGIT Protocol RS232 Protocol Setting

Code Pat

Data

The operation menus and description are shown in the following figures and tables. Figure 1-8(2) is for SPI  $\$  IIC  $\$  PO protocol, and Figure 1-8(3) is for RS232 protocol.

Menu	Setting	Description
Protocol	RS232 SPI IIC PO	To set protocol type, use the knob to choose the protocol and press F1 again.
Protocol Setting		To set the parameters for the protocol.
Code Pat	ALL0 ALL1 01 8PRBS 16PRBS 32PRBS User	To set common code pattern To set user-defined pattern
Data Edit		A shortcut of editing the output data.

Figure 1-8(1) Table 1-1(1) Operation menu description

Figure 1-8(2) Figure 1-8(3) Table 1-1(2) Operation menu description

DIGIT	DIGIT
2/2	2/2
Output Length	Output Length
Rate	Baud 9600
Trigger Auto	Trigger Auto
Channel Setting	Channel Setting

Menu	Setting	Description	
Output Length		To set the data output length.	
#Rate/ Baud		To set the data transmission rate. To set the Baud rate. (In RS232 protocol)	
Trigger		To display the trigger mode.	
Channel Setting		To configure the output channel.	

### Data Edit Interface

User data editor provides abundant data outputs. A DG3000 has a 256k (1k=1024) Bytes of data space. Arbitrary digital waveforms can be generated by editing the data.

Press Arb  $\rightarrow$  Edit Digital W  $\rightarrow$  Data Edit, to enter the "DATAEDIT" interface as shown in Figure 1-9. The interface is divided into three parts: the upper-left side is the Data space, below the Data space is the Input area. The right side of the screen is the Operation Menus.

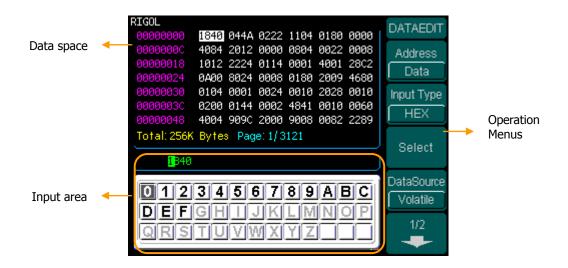


Figure 1-9 User data editing interface

The DATAEDIT operation menus and description are shown in the following figures and tables.

Figure 1-1	0(1)
DATAEDIT	
Address Data	
Input Type HEX	
SELECT	
DataSource	
Volatile	
1/2	

1	Table 1-2(1)	Operation	menu	description
---	--------------	-----------	------	-------------

Menu	Setting	Description	
Address/ Data	Address Data	To edit the address or data.	
Input Type	HEX BIN	To choose hexadecimal or binary form to input user data.	
SELECT		Press this menu to confirm the selecting.	
	Volatile	Data stored in volatile space.	
	ArbWave1		
DataSource	ArbWave2	Data stored in non-volatile space.	
	ArbWave3		
	ArbWave4		

#### Figure 1-10(2)

DataPage UpCross

DataPage DnCross Save Recall

DONE

#### Table 1-2(2) Operation menu description

Menu	Setting	Description
DataPag UpCross		Turn to the last page.
DataPag DnCross		Turn to the next page.
Save Recall		Store user data in non-volatile memory or U disk. Recall data from non-volatile memory or U disk to edit.
DONE		Store all the changes and return the previous level menu.

# Chapter 2 To Edit Digital Waveform

This chapter shows how to edit digital waveforms with respect to the protocol settings. It is divided into the following sections:

- Protocol setting
- To set the code pattern
- To set the data output length
- To set the data transmission rate
- To set the trigger mode
- Channel setting
- Digital waveform calibration

### **Protocol Setting**

Protocol Setting is to configure the format and parameters of the protocol. At present, the DG3000 Logic Signal Output Module supports 4 communication protocols: RS232 (Recommended Standard-232), SPI (Serial Peripheral Interface), IIC (Inter-Integrated Circuit) and PO (Parallel Port). The PO is a parallel communication protocol and the other three are serial communication protocols.

### To Set RS232 Protocol

Press Arb  $\rightarrow$  Edit Digital W  $\rightarrow$  Protocol. Use the knob to choose "RS232". Press F1 again to select "RS232" as shown in Figure 2-1.

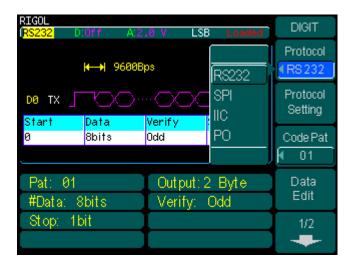
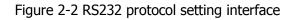


Figure 2-1 Choose RS232 protocol

Press Protocol Setting, to enter the protocol setting menu as shown in Figure 2-2.

RIGOL ( <mark>RS232</mark>	D:Off A:2.	8 V LSB Loaded	- RS232
	<b>₩ →</b> 9600Bp	03	#Data ◀ 8 bits
DØ TX		-0000	Verify
Start	Data	StopBits	I NORE
0	8bits	1bit	#StopBit
			1 bit
Pat: 01		Output: 2 Byte	
#Data:	8bits	Verify: None	1
Stop: 1	bit		DONE



The RS232 protocol setting menu and operation description are shown in the following figure and table.

Figure 2-3 Table 2-1 Operation menu description			
RS 232	Menu	Setting	Description
#Data ≪8 bits Verify	#Data	5bits 6bits 7bits 8bits	To set transmission data length per frame.
✓ None # StopBit ✓ 1 bit	Verify	None Odd Even Fixed0 Fixed1	None Odd Even Fixed0 Fixed1
	#StopBit	1bit 1.5bits 2bits	To set the stop bit of the character frame.
	DONE		Store all the changes and return the previous level menu.

Table 2-1	Operation menu	description
-----------	----------------	-------------

### **Terms Explanation**

To make the setting of "Verify" more flexible for users, two "verify" types: "Fixed0" and "Fixed1" are included.

- Fixed0: Always set the "verify" bit to "0". •
- Fixed1: Always set the "verify" bit to "1". •

### To Set SPI Protocol

Press Arb  $\rightarrow$  Edit Digital W  $\rightarrow$  Protocol. Use the knob to choose "SPI". Press F1 again to select "SPI" as shown in Figure 2-4.

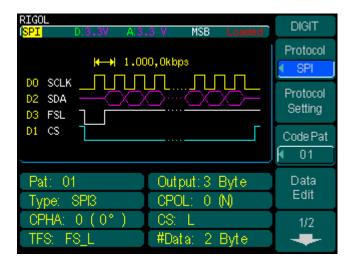


Figure 2-4 Choose SPI protocol

Press Protocol Setting, to enter the protocol setting menu as shown in Figure 2-5.

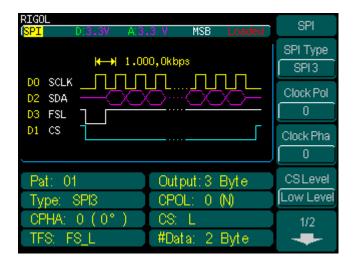


Figure 2-5 SPI protocol setting interface

The SPI protocol setting menu and operation description are shown in the following figures and tables.

SPI
SPI Type
SPI3
Clock Pol
Clock Pha
CSLevel
Low Level
1/2
-

Figure 2-6(1)	Table 2-2(1)	Operation menu description
$I I g u \in Z^{-} U(I)$		

Menu	Setting	Description
SPI Type	SPI3 SPI2	To set the SPI type, use F1 or the knob to switch the options.
Clock Pol	0 1	To set the clock polarity, use F2 or the knob to switch the options.
Clock Pha	0 1	To set the clock phase, use F3 or the knob to switch the options.
CS Level	High Level Low Level	To set the CS level, use F4 or the knob to switch the options.

Figure 2-6(2)

Table 2-2(2) Operation menu description

5	`
SPI	
2/2	
#Data	
TFS	
🖣 FS_L	
DONE	

Menu	Setting	Description
#Data	1Byte 2Byte 3Byte	To set transmission length of each data frame, use the knob to switch the options.
TFS	FS_L FS_H LD_L LD_H NONE	To set the TFS type: Front Sync, Low level effective Front Sync, High level effective Last Sync, Low level effective Last Sync, High level effective Do not the TFS type
DONE		Store all the changes and return the previous level menu.

### **Terms Explanation**

- **SPI Type:** The difference between SPI3 and SPI2 is to set the CS level or not. Users do not want to choose the CS line could select SPI2.
- **TFS:** Transmit Frame synchronization Signal. The frame synchronization signal is used for data synchronization. It indicates the start of the data block.

### To Set IIC Protocol

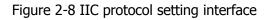
Press Arb  $\rightarrow$  Edit Digital W  $\rightarrow$  Protocol. Use the knob to choose "IIC". Press F1 again to select "IIC" as shown in Figure 2-7.

RIGOL (IIC D:3.3V	A:3.3 V MSB Loaded	DIGIT
	000,0kbps	Protocol
D0 SCLK		Protocol Setting
	rite)	Code Pat
Pat: 01 ID: User 0	Output: 1 Byte Oper: Write	Data Edit
		1/2

Figure 2-7 Choose IIC protocol

Press Protocol Setting to enter the protocol setting menu as shown in Figure 2-8.

RIGOL ( <mark>IIC</mark> D:3.3	V A:3.3	8 V	MSB	Loaded	IIC
₩→ D0 SCLK	1.000,0		_		Address User
D1 SDA		0	Data		User 0
00	Operation O(write)	•			Operation
					Write
Pat: 01 ID: User 0	-+		ut: 1  B :   Wri	-	
					DONE



The operation menu and description of User-defined IIC address are shown in the following figure and table.

Figure 2-9	)
lic	
Address User	
User 0	
Operation Write	
DONE	

Table 2-3	Operation	menu	description
-----------	-----------	------	-------------

Menu	Setting	Description
Address	User None	Enable or disable the Address of the slave or host chip.
User	0~127	Set the Address of the slave or host chip.
Operation	Write Read	To set the operation mode.
DONE		Store all the changes and return the previous level menu.

### To Set PO Protocol

Press Arb  $\rightarrow$  Edit Digital W  $\rightarrow$  Protocol. Use the knob to choose "PO". Press F1 again to select "PO" as shown in Figure 2-10.

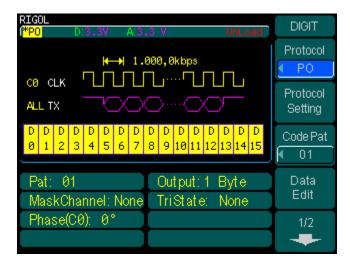


Figure 2-10 Choose PO protocol

Press Protocol Setting, to enter the protocol setting menu as shown in Figure 2-11.

RIGOL (*PO D:3.3V A:3.3 V UnLoad	PO
H→H 1.000,0kbps c0 cLK \\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	Mask Channel
	Tri-state Channel
D D D D D D D D D D D D D D D D D D D 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15	Select
Pat: 01 Output: 1 Byte MaskChannel: None TriState: None	
Phase(C0): 0°	DONE

Figure 2-11 PO protocol setting interface

The PO protocol setting menu and operation description are shown in the following figure and table.

PO
Mask Channel
Tri-state Channel
Select
DONE

Figure 2-12

Table 2-4 Operation menu description

Menu	Setting	Description			
Mask Channel	None D0 : D15	None: No Mask Channel. D0~D15: Select the appointed channel as the Mask Channel.			
Tri-state Channel	None D0 : D15	None: No Tri-state Channel. D0~D15: Select the appointed channels as the Tri-state Channels.			
Select	Select CANCEL	Select or Deselect Mask Channel or Tri-state Channel.			
DONE		Store all the changes and return the previous level menu.			

### Keypoints:

Mask Channel

Data in the Mask Channel can be edited in the user space. "0" in the Mask Channel, indicates the corresponding Tri-state Channel output is not high Z (normal output), while "1" indicates the corresponding Tri-state Channel output is high Z.

• Tri-state Channel

Tri-state Channel is the channel to be "masked". The Tri-state Channel output depends on the corresponding data "1" and "0" in Mask Channel, high Z, and not high Z respectively.

### Example:

Suppose D0 is the Mask Channel, D1 and D5 are Tri-state Channels. Set data in the three channels as shown below:

© 2008 **RIGOL** Technologies, Inc.

#### NOTE

- Only one of the channels (D0~D15) can be chosen as the Mask Channel. The selected channel turns azury on the screen (See page 2-41, Figure 2-45 Channel marks).
- Users can choose one or more channel as the Tri-state Channel. The selected channels turn bolarious on the screen (See page 2-41, Figure 2-45 Channel marks).
- Both the opening (yellow on the screen) and the closed (white on the screen) channels can be chosen as the Mask Channel or the Tri-state Channel. The difference is that the opening channel can output but not the closed channel. To know how to open or close the channel, see page 2-42, Table 2-12(1) Operation menu description.

### To Set the Code Pattern

Code Pattern setting generates the output code patterns. It includes common patterns such as "All0", "All1", "01", PRBS (Pseudo Random Binary Sequence) and user-defined codes.

### Common Code Pattern

Press Arb  $\rightarrow$  Edit Digital W  $\rightarrow$  Code Pat. Choose common code pattern such as: AII0、AII1、01、8PRBS、16PRBS、32PRBS. Use the knob to choose the required code pattern and then press F3, as shown in the Figure 2-13.

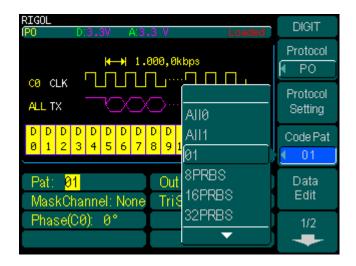


Figure 2-13 Choose the common code patterns

Table 2-5	Description of the Common Code Pattern
-----------	--

Code Pattern	Description
AllO	All "0" sequence
All1	All "1" sequence
01	"0"、"1" alternate sequence
8PRBS	PRBS generated by an 8 bits linear shift register
16PRBS	PRBS generated by a 16 bits linear shift register
32PRBS	PRBS generated by a 32 bits linear shift register

### User Define Code Pattern

Press Arb  $\rightarrow$  Edit Digital W  $\rightarrow$  Code Pat  $\rightarrow$  User. Scroll until "User" appears in the menu bar as shown in Figure 2-14. Users can set the data output offset as needed.

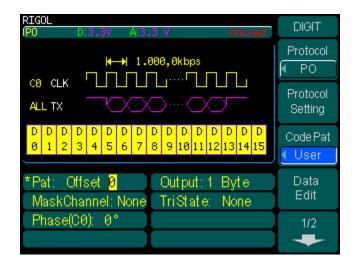


Figure	2-14	Set	the	output	offset
--------	------	-----	-----	--------	--------

When "User" is selected, the outputs of the generator will be the user edited data (See the next section "User data editor"). The "Offset" setting is convenient for users to choose the starting position of output data.

### **User Data Editor**

### Edit Address and Data

#### 1. Edit address

Select Address or Data to edit; press F1 to toggle the two options. Select Address as shown in Figure 2-15.

RIGOL 200000000 00000000 000000018 000000024 000000030 000000030	4084 201 1012 222 0A00 802 0104 000	4 0008 11 0024	0804 0001 0180 0010	0022 ( 4001 ( 2009 - 2028 (	0008 28C2 4680 0010	DATAEDIT Address Data Input Type
0000003C 00000048 Total: 256K 00000	Bytes P	IC 2000	9008	0010 ( 0082 ;	3060 2289	HEX Select
O 1 2 3 4 5 6 7 8 9 A B C DEFGHIJKLMNOP QRSTUVWXYZ						DataSource Volatile 1/2

Figure 2-15 Select "Address" to edit

When editing address, use the left or right direction key to select the address bit. Use the knob to choose the numeric or letter on the displayed keyboard. Press Select to confirm the selection.

### PROMPT

• Address can only be set to an even number, or an error will appear.

© 2008 **RIGOL** Technologies, Inc.

### 2. Edit data

Press F1 and select Data, and then modify the data on current highlighted position. There are two kinds of input types: Hexadecimal and Binary. Use the left or right direction key to select the data bit for editing. Use the knob to choose the numeric or letter on the displayed keyboard. Press **SELECT** to confirm the selection. The input format of Hexadecimal and Binary are shown in Figure 2-16.

RIGOL 00000000 00000000 00000018 00000024 00000030 00000030 00000030 00000048 Total: 256K	1012 0A00 0104 0200 4004	2012 2224 8024 0001 0144 909C	0000 0114 0008 0024 0002 2000	0804 0001 0180 0010 4841 9008	2009 2028 0010	0008 28C2 4680 0010 0060	DATAEDIT Address Data Input Type HEX Select
DEF QRS	3 4 G H T U	56	6 7 J K V X	8 9 L M Y Z			DataSource Volatile 1/2

Figure 2-16(a) Edit data (Hexadecimal)

000000000 00000018 00000024 00000030 00000030 00000030	4084 2012 1012 2224 0A00 8024 0104 0001 0200 0144 4004 909C	0000 0804 0114 0001 0008 0180 0024 0010 0002 4841 2000 9008	4001 28C2 2009 4680 2028 0010	DATAEDIT Address Data Input Type BIN
Total: 256K	Select			
D123456789ABC DEFGHIJKLMNOP QRSTUVWXYZ				DataSource Volatile 1/2

Figure 2-16(b) Edit data (Binary)

# Edit Stored Digital Waveform

The currently editing data is in the volatile memory, and the stored data in the non-volatile memory can be imported as shown in Figure 2-17.

RIGOL       1840       044A       0222       1104       0180       0000         00000000       4084       2012       0000       0804       0022       0000         000000018       1012       2224       0114       0001       4001       28C2         00000024       0A00       8024       0008       0180       2009       4680         00000024       0104       0001       0024       0010       2028       0010         00000030       0104       0001       0024       4841       0010       0060         00000032       0200       0144       0002       4841       0010       0060         00000034       4004       909C       2080       9088       0082       2289	DATAEDIT Address Data Input Type HEX
Total: 256K Bytes Page: 1/3121	Select
D123456789ABC DEFGHIJKLMNOP QRSTUVWXYZ	DataSource ArbWave4 1/2

Figure 2-17 Select data source

Press DataSource. Repeat pressing this key or use the knob to select the storage location of digital waveform: Volatile 、ArbWave1 、ArbWave2 、ArbWave3 or ArbWave4.

### PROMPT

- Only the location which has stored digital waveform will appear in the DataSource option.
- The currently editing data is always located in the volatile memory. When choosing data from the non-volatile memories: ArbWave1、ArbWave2、ArbWave3 or ArbWave4; data will be copied to volatile memory for editing, and those stored in non-volatile will not be changed.

# Data Browse, Store and Recall

### 1. Data browse

Press F5 and switch to the second page as shown in Figure 2-18. Press DataPage UpCross to display data in the previous page, press DataPage DnCross you will see data in the next page.

RIGOL	DATAEDIT
00000054 4111 0408 0060 2081 0000 8004	
00000060 0018 A001 0000 2692 3A24 A208	2/2
0000006C 1002 2448 0100 8050 0010 01C2	-
00000078  0000 A000 1840 8800 0441 500A	_
0000084 0140 4800 8000 0003 0040 0069	DataPage
00000090 0000 2110 0001 A280 C011 2880	UpCross
0000009C 0001 0A80 1000 080C 0200 8080	Opeross
Total: 256K Bytes Page: 2/3121	DataPage
3111	DnCross
0123456789ABC	Save
U 1 2 3 4 3 0 7 0 9 A D C	Recall
DEFGHIJKLMNOP	
	DONE
	DONE

Figure 2-18 Switch the data page

Below the Data Space, it shows the total data length and the current page. If users want to edit data on any page; switch to that page by pressing DataPage UpCross or DataPage DnCross. Press F1 to return the upper level menu, and then edit data as introduced before.

#### RIGOL

### 2. Save and Recall

Users can store data in the non-volatile memory or a U disk, and recall them into the volatile space for editing.

Press Save Recall to enter the save and recall interface as shown in Figure 2-19.



Figure 2-19 Save and recall interface

The Save/Recall menu and operation description are shown in the following figures and tables.

STORAGE	
File Type	
Browser	
File	
Save	
Recall	
1/2	

Figure 2-20(1)

Table 2-6(1) Operation menu description
---

	Menu	Setting	Description
	File Type	Data All File State	Arbitrary Waveform files All kinds of files The Setting of the generator
	Browser	Directory Path File	Shift between Directory, Path and file
	Save		Save digital waveform data to the appointed place
	Recall		Recall the waveform or Setting information in the specific position in the memory.

```
Figure 2-20(2)
```



Menu	Setting	Description		
Delete		Delete the selected waveform file.		
CANCEL		Cancel the current operation and return the previous level menu.		

## File type selection

Press F1 repeatedly to toggle between the file types.

## To use the Browser

Press F2 repeatedly to toggle between "Directory", "Path" and "file".

### RIGOL

## (1) **Save**

Press Save, to enter the data saving interface. Enter a filename by using the displayed keyboard. The instrument supports both Chinese and English inputs.

## • Chinese input

Example: To enter the filename "数字波". Press F1 to select Chn\_S.



Figure 2-21 Input Chinese filename

Use the knob to choose "s"; press SELECT to confirm the selection. Repeat with "h" and "u"; the input field will display a series of matching Chinese characters. Use the direction key to look for the characters "数" as shown in Figure 2-21. Press "4" in the keypad on the front panel of the DG3000. Repeat the above process to input "字" and "波". To delete a character, use the direction key to select it and then press Delete. When finished, press End/Store to end the storage.

### • English input

Example: To enter the filename "digital" as shown in Figure 2-22. Press F1 to select En.

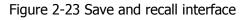
RIGOL File Name	STORAGE
digital	Input Type En
	SELECT
	Delete
0123456789_ab cdefghijkumno	End / Store
pqrstuvwxyz	CANCEL

Figure 2-22 Input English filename

Use the knob to choose "d"; press SELECT to confirm the selection. Repeat until "digital" completed. To delete a character, use the direction key to select it and then press Delete. When finished, press End/Store to end the storage.

The final results after the examples above are as shown in Figure 2-23.





## (2) Recall

As shown in Figure 2-24, there is a file named "digital" in the non-volatile location ARB2.

RIGOL C \ARB2: digital Local (C:) ARB1: ARB2: digital	STORAGE File Type Data
ARB3: ARB4: 0	Browser File
	Save
Success to read file. Complete. Digital waveform data of use	Recall
r space have been changed.	1/2

Figure 2-24 Recall a stored digital waveform

Press F2 to switch the browser and select "File". Use the up and down direction key to select "ARB2: digital". Press Recall to recall the file. The screen will display a prompt as shown in figure 2-24.

#### NOTE

 There will be an error when trying to read analog waveform to digital storage space. When you enter the STORAGE menu from "user data editor", only digital waveform can be read. If try to read an analog waveform, an error message will be given (See the "Error Message" section).
 There will be an error when trying to read digital waveform to analog storage space. When you enter the STORAGE menu by pressing STORAGE on the front

When you enter the STORAGE menu by pressing <u>STORAGE</u> on the front panel of DG3000, only analog waveform can be read, if try to read a digital waveform, an error message will be given (See the "Error Message" section).

### (3) Delete

Enter the second page of the STORAGE menu as shown in Figure 2-25; use the knob and direction key to select the file you want to delete, and then press Delete.

RIGOL C \ARB2: digital		STORAGE
Local (C:)	ARB1: ARB2: digital	2/2
	ARB3: ARB4: 0	Delete
		CANCEL

Figure 2-25 File delete

# Output User Data

When Code Pat is set to "User", the protocol will output the user-defined data, and users can set the value of "Offset" to select the start data location.

The communication protocols have specified transmission sequence of data. For example, RS232 uses "LSB"; SPI and IIC use "MSB", but for PO, both "LSB" and "MSB" can be used. Therefore, the output sequence of user data depends on the choosen protocol.

### LSB:

Least Significant Bit, for binary sequence 00001111, LSB is the most right bit `1''.

### MSB:

Most Significant Bit, for binary sequence 00001111, MSB is the most left bit "0".

In the following **Example 1** and **Example 2** we will introduce how to output user-defined data.

RIGOL 000000000 00000000 00000018 00000024 00000020 00000030	4084 2012 1012 2224 0A00 8024 0104 0001 0200 0144	0114 000 0008 018 0024 001 0002 484	4 0022 1 4001 0 2009 0 2028 1 0010	0008 28C2 4680 0010 0060	DATAEDIT Address Data Input Type
00000048 Total: 256K		: 2000-900 ge:1/3121	8 0082	2289	Select
012 DEF QRS	345 GHI TUV	678 JKL MXY	9 A E M N C Z		DataSource Volatile 1/2

Figure 2-26 Output user data

## Example 1:

Use RS232 protocol, output data length=1 Byte, data line=D1, Offset=1, the other parameters use default value, output user data as shown in Figure 2-26.

Explanation:

Offset=1 indicates the first data "1840" would not be output. As data transmitting through line D1, it will output 8 bits (1 Byte) in the 8 data start from "044A". The data storage is shown in the following table.

© 2008 RIGOL Technologies, Inc.

Table 2-7 Data Storage	Table	2-7	Data	Storage
------------------------	-------	-----	------	---------

Address	Data (D15~D0)
0000002	044A(0000010001001010)
0000004	0222(0000001000100010)
0000006	1104(000100010000100)
0000008	0180(00000011000000)
A000000A	0000(000000000000000)
000000C	4084(010000010000100)
000000E	2012(001000000010010)
0000010	0000(000000000000000)

Output:

- 1. According to the above table, the output data would be: 1 1 0 0 0 0 1 0;
- 2. For RS232 protocol, the output sequence is "LSB", then the output should be: 0 1 0 0 0 0 1 1;
- 3. For RS232 protocol is negative logic output, in the output figure, "0" is represented by high level and "1" is represented by low level as shown in the figure below:

0	1	0	0	0	0	1	1	             

Note: For RS232 protocol, -15V $\sim$ -5V is logic "1" and +5V $\sim$ +15V is logic "0".

# Example 2:

Use PO protocol, output data length=4Bytes, data line=All, Offset=0, the other parameters use default value, output user data as shown in Figure 2-26.

Explanation:

Offset=0 indicates the first data "1840" would be output. As data transmitting parallel from the 16 channels, each channel will output 2 bits (total is 32 bits=4 Bytes). The data storage is shown in the following table.

Table 2-6 Data Stolage (+Dytes)				
Address	Data (D15~D0)			
0000000	1840(0001100001000000)			
0000002	044A(0000010001001010)			

Table 2-8 Data Storage (4Bytes)

© 2008 RIGOL Technologies, Inc.

The output is shown in the following figure:

SCLK			
	0	0	/
D15	0	0	
<b>D</b> <sub>14</sub>	0	0	
D13	1	0	
<b>D</b> 12	1	0	
<b>D</b> 11			
<b>D</b> 10	0	1	
D9	0	0	
	0	0	             
D8	0	0	
D7	0	1	 
D6			
D5	0	0	
D4	0	0	
D3	0	1	
D3 D2	0	0	
	0	1	 
<b>D</b> 1	0	0	
Do	0	0	
		1 1 1 1 1	       

# To Set the Output Data Length

Press Arb  $\rightarrow$  Edit Digital W  $\rightarrow$  F5, to enter the second page of "DIGIT". Press Output Length to set the output data length as shown in Figure 2-27.

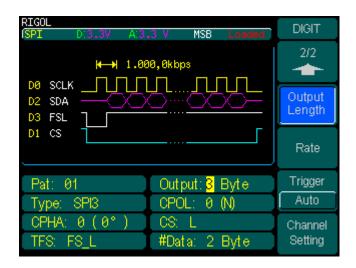


Figure 2-27 Set the output data length

## PROMPT

- **Output Length:** To define the output data length (Byte). Different protocols have different upper limit.
- If current the current output code is User code, then User code plus User offset can not exceed User space and the upper limit of protocol output, in addition, not to exceed 256k Bytes.
- RS232 output upper limit: 35k Bytes; SPI output upper limit: 40k Bytes; IIC output upper limit: 10k Bytes; PO output upper limit: 128k (1k=1024) Byte.

# To Set the Data Transmission Rate

Different protocols have different data transmission rate settings. RS232 refers to "Baud per second", while SPI, IIC, PO refer to set "bits per second (bps)".

#### 1. Baud rate Setting

Press Arb  $\rightarrow$  Edit Digital W  $\rightarrow$  F1  $\rightarrow$  RS232  $\rightarrow$  F5, to enter the second page of "DIGIT" menu. Press Baud, it would display as shown in Figure 2-28.

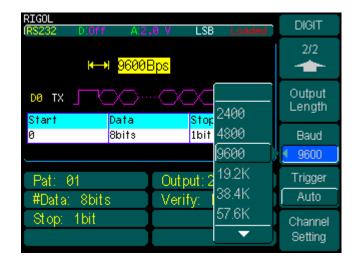


Figure 2-28 Set the Baud rate

Baud setting provides transmission rate options: 2400, 4800, 9600, 19.2k, 38.4k, 57.6k, 115k, and the default is 9600.

Besides the above options, the RS232 also provides user-defined output rate as shown in Figure 2-29.

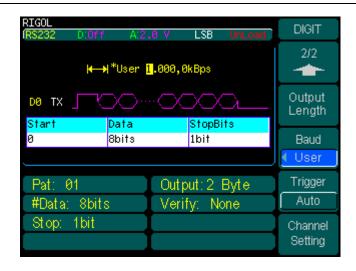


Figure 2-29(a) Choose user-defined output rate

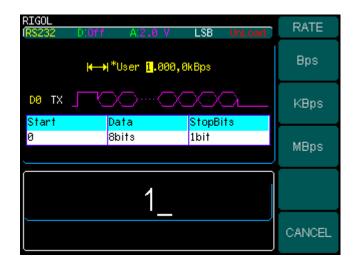


Figure 2-31(b) Input user-defined output rate

### Terms Explanation

- **Baud rate**: Code elements transmission per second. The unit is Bps (Bauds per second).
- **Bit rate**: Binary bits transmission per second. The unit is bps (bits per second). Bit rate is an important specification of digital communication system. When the channel is fixed, the higher the information transmission rate is, the better the effectiveness will be.

© 2008 **RIGOL** Technologies, Inc.

### 2. Rate Setting

Press Arb  $\rightarrow$  Edit Digital W  $\rightarrow$  F1  $\rightarrow$  SPI  $\rightarrow$  F5, to enter the second page of "DIGIT". Press Rate, it would display as shown in Figure 2-30.

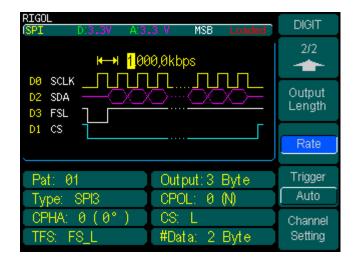


Figure 2-30 Transmission rate setting

# To Set the Trigger Mode

The module provides 2 trigger modes: Auto and Burst, the default is Auto mode.

### 1. Auto trigger

Press Arb  $\rightarrow$  Edit Digital W  $\rightarrow$  F5  $\rightarrow$  Trigger, to enter the interface as shown in Figure 2-31. To change the trigger mode; turn to Burst mode.

RIGOL ISPI D:3.3V A:3.3 V MSB Loaded	DIGIT
<mark>₩→</mark> ₩ <mark>1</mark> ,000,0kbps	2/2
D8 SCLK L L L L L L L L D2 SDA C C C D3 FSL	Output Length
	Rate
Just displays the trigger mode. It will be changed when burst mode is used	Trigger Auto
	Channel Setting

Figure 2-31 Set Auto trigger mode

## 2. Burst mode trigger

Burst trigger includes 3 modes with respect to different trigger sources: Internal, External and Manual. In Burst mode, the screen will also display the current protocol in use and the related parameters.

# 1) Internal trigger

Press Burst  $\rightarrow$  Source  $\rightarrow$  Internal, to enter the interface as shown in Figure 2-32.

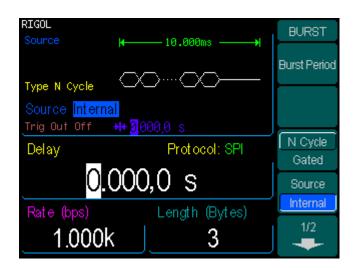


Figure 2-32 Choose internal source

Press F5, to enter the second page of "BURST" as shown in Figure 2-33. Select a rising or a falling edge through the Trig Out option, or you can choose "Off" to disable the trigger output.

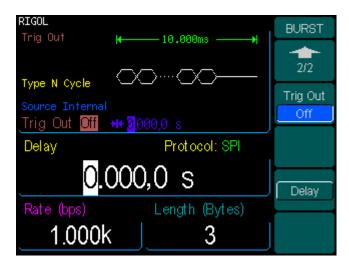


Figure 2-33 Set the "trig out" signal

### 2) External trigger

Press Burst  $\rightarrow$  Source  $\rightarrow$  External, to enter the following interface as shown in Figure 2-34.

RIGOL	BURST
Source	Borton
Source External	
Slope F +1+ 0000,0 s	
Delay Protocol: SPI	N Cycle
	Gated
<mark>0</mark> .000,0 s	Source
Rate (bps) Length (Bytes)	External
1.000k 3	1/2

Figure 2-34 choose external source

Press F5, to enter the second page of "BURST" as shown in Figure 2-35, and select a rising or a falling edge through the Slope option.

RIGOL	BURST
	2/2
Type N Cycle	Slope
Source External	<u> </u>
Delay Protocol: SPI	
<mark>0</mark> .000,0 s	Delay
Rate (bps) Length (Bytes)	

Figure 2-35 Set the "trig out" signal

RIGOL

### 3) Manual trigger

Press Burst  $\rightarrow$  Source  $\rightarrow$  Manual, to enter the interface as shown in Figure 2-36. The function key Trigger on the front panel will turn bright when Manual is selected.

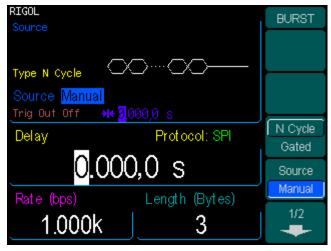


Figure 2-36 Choose manual trigger

Press F5, to enter the second page of "BURST" as shown in Figure 2-37. Select a rising or a falling edge through the Trig Out option, or you can choose "Off" to disable the trigger output.

RIGOL	BURST
Trig Out	
	2/2
Type N Cycle	212
	Trig Out
Source Manual	Off
Trig Out Off 🗰 🛛 🖓 000,0 s	
Delay Protocol: SPI	
<mark>0</mark> .000,0 s	Delay
Rate (bps) Length (Bytes)	

Figure 2-37 Set the "trig out" signal

When finishing the trigger mode setting, press Burst to return the "DIGIT" interface.

# **Channel Setting**

The module provides 16 data output lines and 2 clock lines. Users can select any of them as in need, and set the channel output voltage. This section introduces the channel settings of each protocol.

# Channel Setting for RS232 Protocol

Press Arb  $\rightarrow$  Edit Digital W  $\rightarrow$  RS232  $\rightarrow$  Channel Setting, to enter the interface as shown in Figure 2-38.

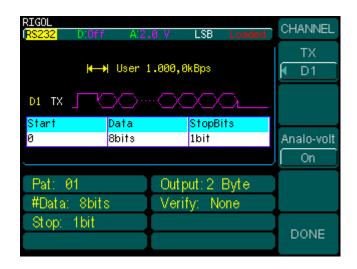


Figure 2-38 Channel setting of RS232

The Channel setting menu of RS232 protocol and operation description are shown in the following figure and table.

CHANNEL
TX
M D1
Analo-volt
On
DONE

Figure 2-39

Table 2-9 Operation menu description

Menu	Setting	Description
ТХ	D0 D1 : D14 D15	Select data line of RS232.
Analo-volt	On Off	Set analog channels voltage.
DONE		Store all the changes and return the previous level menu.

### PROMPT

Since RS232 voltage is negative logic and has negative voltage. While digital channel cannot output negative voltage, just enable analog voltage and always close digital channel.

Channel Setting for SPI Protocol

Press Arb  $\rightarrow$  Edit Digital W  $\rightarrow$  SPI  $\rightarrow$  Channel Setting, to enter the interface as shown in Figure 2-40.

RIGOL		CHANNEL	
I <mark>SPI D:3.3V A</mark> :3	.3 V MSB Loaded		
		SCLK	
₩→ 1.00			
DO SCLK			
		SDA	
D3 FSL 7			
D1 CS			
	CS		
	🖣 D1		
Pat: 01	Output:3 Byte	TFS	
	· · · · · · · · · · · · · · · · · · ·		
Type: SPI3	CPOL: 0 (N)	<b>∢</b> D3	
CPHA: 0 (0°)	CS: L	1/2	
TFS: FS_L	#Data: 2 Byte	-	

Figure 2-40 Channel setting of SPI

The Channel setting menu of SPI protocol and operation description are shown in the following figures and tables.

Figure 2-41(1)
----------------

Table 2-10(1) Operation menu description

CHANNEL	Menu	Setting
SCLK		D0
	SCLK	:
		D15
SDA		D0
(D2)	SDA	:
CS		D15
D1		D0
	CS	:
TFS		D15
(D3)		D0
1/2	TFS	:
		D15

SCLK	D0 :	Select clock line of SPI.
	D15	
	D0	
SDA	:	Select data line of SPI.
	D15	
	D0	
CS	:	Select CS line of SPI.
	D15	
	D0	
TFS	:	Select TFS line of SPI.
	D15	

Description

CHANNEL	Menu	Setting	Description
2/2 → Digit-Volt ( 3.3V	Digit-Volt	1.8V 2.5V 3.3V 4.0V User Off	Set digital channels voltage. The default is 3.3V. User-defined mode (1.4V~4.2V) Turn off digital voltage output.
Analo-volt On	Analo-Volt	On Off	Set analog channels voltage.
	DONE		Store all the changes and return the previous level menu.
DONE			

#### Figure 2-41(2) Table 2-10(2) Operation menu description

## NOTE

- When setting SCLK, SDA, CS and TFS, selected D0~D15 cannot be double set. For example, SCLK has set to be D0; SDA cannot be assigned to D0, unless SCLK being set to another line.
- SPI, IIC, PO protocols provide both analog and digital channel voltage output. Users can configure them individually.
- When analog voltage is higher than 4.2V, digital voltage will automatically adjust to be 3/8 of analog voltage.

# Channel Setting for IIC Protocol

Press Arb  $\rightarrow$  Edit Digital W  $\rightarrow$  IIC  $\rightarrow$  Channel Setting, to enter the interface as shown in Figure 2-42.

RIGOL ( <mark>IIC</mark> D:3.3V A	3.3 V MSB Loaded	CHANNEL
₩→) 1.00 DO SCLK ] [] [	SCLK Ø D0	
	SDA D1	
Addr(hex) Opera 00 0(wri		Digit-Volt ◀ 3.3V
Pat: 01 ID: User 0	Output: 1 Byte Oper: Write	Analo-volt On
		DONE

Figure 2-42 Channel setting of IIC

The Channel setting menu of IIC protocol and operation description are shown in the following figure and table.

CHANNEL		
SCLK		
K DO		
SDA		
🖣 D1		
Digit-Volt		
<ul><li>◀ 3.3V</li></ul>		
Analo-volt		
On		
DONE		

Figure 2-43

Menu	Setting	Description
SCLK	D0 : D15	Select clock line of IIC.
SDA	D0 : D15	Select data line of IIC.
Digit-Volt	1.8V 2.5V 3.3V 4.0V User Off	Set digital channels voltage. The default is 3.3V. User-defined mode(1.4V~4.2V) Turn off digital voltage output.
Analo-Volt	On Off	Set analog channels voltage.
DONE		Store all the changes and return the previous level menu.

# NOTE

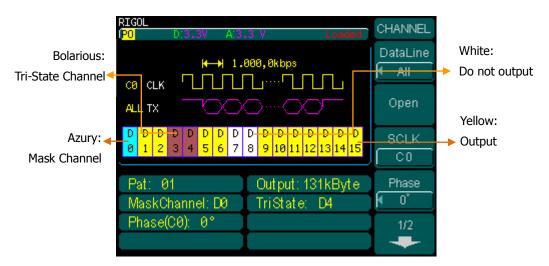
When setting SCLK, SDA, CS and TFS, selected D0 $\sim$ D15 cannot be double set. For example, SCLK has set to be D0; SDA cannot be assigned to D0, unless SCLK is being set to another line. Channel Setting for PO Protocol

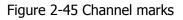
Press Arb  $\rightarrow$  Edit Digital W  $\rightarrow$  PO  $\rightarrow$  Channel Setting, to enter the interface as shown in Figure 2-44.

RIGOL PO D:3.3V A:3.3 V Loaded	CHANNEL
— ₩→₩ 1.000,0kbps c0 cLK 「↓↓↓↓↓↓	DataLine
	Close
D       D	SCLK
Pat: Offset 0 Output: 1 Byte MaskChannel: None TriState: None	Phase
Phase(C0): 0°	1/2

Figure 2-44 Channel setting of PO

PO is a parallel protocol. Users can choose multiple channels and set their output status (ON or OFF). As introduced in page 2-9 "To Set PO Protocol", the protocol can set Mask channel and Tri-State channel. These channels can be identified with different colors as the following figure shows.





The Channel setting menu of PO protocol and operation description are shown in the following figures and tables.

CHANNEL
DataLine
🔍 Ali
Close
SCLK
C0
Phase
<b>●</b> 0°
1/2

Menu	Setting	Description
DataLine	D0 : D15 ALL	Select data line of PO.
Open Close		To confirm whether output the selected channel or not. Open denotes output. Close denotes do not output.
SCLK	C0 C1	Select clock line of PO.
Phase	0 90 180 270	Select the clock phase.

Figure 2-46(1)	Tahla 2-12(1)	Operation menu description
$1$ igule $2^{-4}0(1)$		Operation menu description

Figure	2-46	(2)
riguic	2 101	~/

Table 2-12(2) Operation menu description



Menu	Setting	Description
Digit-Volt		Set digital channels voltage. The default is 3.3V. User-defined mode (1.4V $\sim$ 4.2V)
Analo-Volt	Off On Off	Turn off digital voltage output. Set analog channels voltage.
DONE		Store all the changes and return the previous level menu.

# **Digital Waveform Calibration**

Digital Waveform Calibration is used to calibrate the output voltage of the module. When users connect the digital module with different DG3000s or connect a DG3000 with different modules, the calibration should be performed.

Press Arb  $\rightarrow$  Cal Digital W, to enter the interface as shown in Figure 2-47.

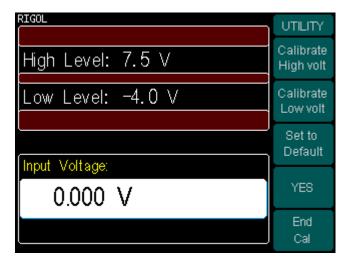


Figure 2-47 Digital waveform calibration interface

rigure 2 40 lable 2 15 Operation mend description					
UTILITY		Menu	Setting	Description	
Calibrate High volt		Calibrate High volt		Select the High Level to be calibrated.	
Calibrate Low volt		Calibrate Low volt		Select the Low Level to be calibrated.	
Set to Default		Set to Default		Restore to default calibration.	
YES	1	YES		Press the button to complete the selected calibration item.	
End Cal		End Cal		Finish the calibration.	

### Figure 2-48

Table 2-13 Operation menu description

### 1. Calibration method

Turn on the digital waveform calibration function, use the logic analyzer testing line or probe of the oscilloscope and connect the output signal of the analog channel to an oscilloscope or a multimeter.

## 2. Calibration step

(1) High level calibration

Press Calibration High volt and read the DC voltage level from the oscilloscope or the multimeter when the output is high level of 7.5V, input the value to DG3000 and press YES to finish the calibration.

(2) Low level calibration

Press Calibration Low volt, and read the DC voltage level from the oscilloscope or the multimeter when the output is low level of -4V, input the value to DG3000 and press YES to finish the calibration.

If there are input errors or other cause and results in error of the output voltage after the calibration, press Set to Default to restore the level to factory default.

# Chapter 3 Examples

This chapter provides users application examples of digital waveform generation. They are:

- Example 1: To generator a RS232 digital waveform
- Example 2: To generator a SPI digital waveform
- Example 3: To generator an IIC digital waveform
- Example 4: To generator a PO digital waveform
- Example 5: To generate an IIC digital waveform using the PO protocol

# Example 1: To Generate a RS232 Digital Waveform

To generate a RS232 protocol digital waveform: 8 bits per frame of data transmission, Odd verification, 1 Stopbit, "01" code pattern, 1 Byte of output data length, 9600Bps of baud rate, Auto trigger mode, data lines D0, analog voltage output of 2.0V.

## **Operation steps:**

- 1. Press  $Arb \rightarrow Edit Digital W$ , to initiate the digital waveform editing interface.
- 2. Press Protocol, and use the knob to choose "RS232". Press the softkey again to select "RS232".
- 3. Press Protocol Setting, for the menu of RS232 protocol settings.
  - 1) Press **#Data**; use the knob to choose "8bits". Press the softkey again to select "8bits".
  - Press Verify; use the knob to choose "Odd". Press the softkey again to select "Odd".
  - 3) Press **#**StopBit; use the knob to choose "1bit". Press the softkey again to select "1bit".
  - 4) Press DONE to complete the RS232 protocol settings.
- 4. Press Code Pat, and use the knob to choose "01". Press the softkey again to select "01" code.
- 5. Press F5 to go to the next page.
- 6. Press Output Lenth, and use the keyboard input 1. Press Enter, (or use the direction key and the knob to input the value) the output of data length is set to be 1 Byte.
- 7. Press Baud, and use the knob to choose "9600". Press the softkey again to select 9600Bps.
- 8. Press Trigger, the default is "Auto". (You cannot set the trigger mode through this menu. To understand the other trigger mode, please refer to the section of "To Set the Trigger Mode" in chapter 2.)
- 9. Press Channel Setting, for the menu of channel settings.
  - 1) Press **TX**; use the knob to choose "D0" from optional D0~D15. Press the

softkey again to select "D0".

- 2) Press Analo-volt; use the keyboard to input "2" and choose the unit of "V", then the output analog voltage is set to be "2.0V".
- 3) Press DONE to complete the channel settings.
- 10. Press the blinking button Arb. All modified parameters (denoted with "\*") will be implemented. The "\*" sign will disappear, and the configuration status will change from "UnLoad" to "Loaded".

After all the above steps are finished, the screen display will be shown as in Figure 3-1.

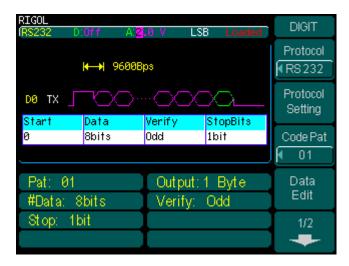


Figure 3-1 RS232 protocol output

# Example 2: To Generate a SPI Digital Waveform

To generate a SPI protocol digital waveform: SPI3 type, 0 clock polarity, 0 clock phase, low CS lene D0, dvel, 2 Bytes per frame of data transmission, TFS type FS\_L, "01" code pattern, 2 Bytes of output data length, 1 kbps of data transmission rate, Auto trigger mode, clock liata line D2, CS line D1, Start-stop mark line D3, digital voltage output of 3.3V.

## **Operation steps:**

- 1. Press  $Arb \rightarrow Edit Digital W$ , to initiate the digital waveform editing interface.
- 2. Press Protocol, and use the knob to choose "SPI". Press the softkey again to select "SPI".
- 3. Press Protocol Setting, for the menu of SPI protocol settings.
  - 1) Press SPI Type; use the softkey or the knob to select "SPI3".
  - 2) Press Clock Pol; use the softkey or the knob to select "0".
  - 3) Press Clock Pha; use the softkey or the knob to select "0".
  - 4) Press CS Level; use the softkey or the knob to select "Low Level".
  - 5) Press F5 to enter the next page.
  - 6) Press #Data; use the keyboard to input "2".
  - 7) Press **TFS**; use the knob to choose "FS\_L". Press the softkey again to select "FS\_L".
  - 8) Press DONE to complete the SPI protocol setting.
- 4. Press Code Pat; use the knob to choose "01". Press the softkey again to select "01" code.
- 5. Press F5 to go to the next page.
- 6. Press Output Lenth, and use the keyboard input 2. Press Enter, (or use the direction key and the knob to input the value.) the output of data length is set to be 2 Bytes.
- 7. Press Rate, and use the keyboard to input "1". Choose the unit of "kbps". The data transmission rate is set to be 1 kbps.
- 8. Press Trigger, the default is "Auto". (You cannot set the trigger mode through this menu. To understand the other trigger mode, please refer to the section of "To Set the Trigger Mode" in chapter 2.)

© 2008 **RIGOL** Technologies, Inc.

- 9. Press Channel Setting, for the menu of channel settings.
  - 1) Press SCLK; use the knob to choose "D0". Press the softkey again to select "D0".
  - Press SDA; use the knob to choose "D2". Press the softkey again to select "D2".
  - 3) Press CS; use the knob to choose "D1". Press the softkey again to select "D1".
  - Press TFS; use the knob to choose "D3". Press the softkey again to select "D3".
  - 5) Press F5 to go to the next page.
  - 6) Press Digit-Volt; use the knob to choose "3.3V". Press the softkey again to select "3.3V". Please observe that the softkey Analo-volt is "ON" and has been automatically set to be 3.3V.
  - 7) Press DONE to complete the channel settings.
- 10. Press the blinking button Arb. All modified parameters (denoted with "\*") will be implemented. The "\*" sign will disappear, and the configuration status will change from "UnLoad" to "Loaded".

After all the above steps are finished, the screen display will be shown as in Figure 3-2.

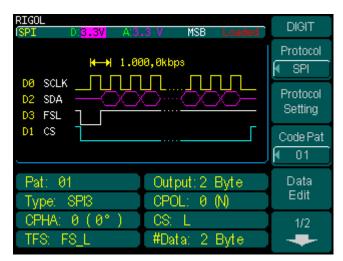


Figure 3-2 SPI protocol output

# Example 3: To Generate an IIC Digital Waveform

To generate an IIC protocol digital waveform: User address 10, Write operation mode, "All0" code pattern, 2Bytes of output data length, 1kbps of data transmission rate, Auto trigger mode, clock line D0, data line D1, digital voltage output of 3.3V.

## **Operation steps:**

- 1. Press  $Arb \rightarrow Edit Digital W$ , to initiate the digital waveform editing interface.
- 2. Press Protocol, and use the knob to choose "IIC". Press the softkey again to select "IIC".
- 3. Press Protocol Setting, for the menu of IIC protocol settings.
  - 1) Press Address; use the softkey or the knob to choose "User".
  - 2) Press User; use the knob to change the address to be "10". Press the softkey again to finish the setting.
  - 3) Press Operation; choose "Write". Press the softkey again to finish the operation mode setting.
  - 4) Press DONE to complete the IIC protocol settings.
- 4. Press Code Pat, and use the knob to choose "Allo". Press the softkey again to select "Allo" code.
- 5. Press F5 to go to the next page.
- 6. Press Output Lenth, and use the keyboard input 2. Press Enter, (or use the direction key and the knob to input the value.) the output of data length is set to be 2 Bytes.
- 7. Press Rate, and use the keyboard to input "1". Choose the unit of "kbps". The data transmission rate is set to be 1kbps.
- 8. Press Trigger, the default is "Auto". (You cannot set the trigger mode through this menu. To understand the other trigger mode, please refer to the section of "To Set the Trigger Mode" in chapter 2.)
- 9. Press Channel Setting, for the menu of channel settings.
  - 1) Press SCLK, and use the knob to choose "D0". Press the softkey again to select "D0".

- Press SDA, and use the knob to choose "D1". Press the softkey again to select "D1".
- 3) Press Digit-Volt, and use the knob to choose "3.3V". Press the softkey again to select "3.3V". Please observe the moment you will see the softkey Analo-volt is "ON" and has been automatically set to be 3.3V.
- 4) Press DONE to complete the channel settings.
- 10. Press the blinking button Arb. All modified parameters (denoted with "\*") will be implemented. The "\*" sign will disappear, and the configuration status will change from "UnLoad" to "Loaded".

After all the above steps are finished, the screen display will be shown as in Figure 3-3.

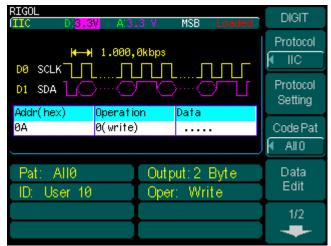


Figure 3-3 IIC protocol output

### Example 4: To Generate a PO Digital Waveform

To generate a PO protocol digital waveform: Mask Channel of D0, Tri-State Channel of D3 and D4, "8 PRBS" code pattern, 2 Bytes of output data length, 1k bps of data transmission rate, Auto trigger mode, "All" data line, clock line C1, 90 phase, turn off digital voltage output and output analog voltage of 1.8V.

#### **Operation steps:**

- 1. Press  $Arb \rightarrow Edit Digital W$ , to initiate the digital waveform editing interface.
- 2. Press Protocol, and use the knob to choose "PO". Press the softkey again to select "PO".
- 3. Press Protocol Setting, for the menu of PO protocol settings.
  - 1) Press Mask Channel; use the knob to choose "D0". Press F3 to select it.
  - 2) Press Tri-state Channel; use the knob to choose "D3". Press F3 to select it, use the same way to select D4.
  - 3) Press DONE to complete the PO protocol settings.
- 4. Press Code Pat, and use the knob to choose "8PRBS". Press the softkey again to select "8PRBS" code.
- 5. Press F5 to go to the next page.
- 6. Press Output Length, and use the keyboard input 2. Press Enter, (or use the direction key and the knob to input the value.) the output of data length is set to be 2 Bytes.
- 7. Press Rate, and use the keyboard to input "1". Choose the unit of "kbps". The data transmission rate is set to be 1 kbps.
- 8. Press Trigger, the default is "Auto". (You cannot set the trigger mode through this menu. To understand the other trigger mode, please refer to the section of "To Set the Trigger Mode" in chapter 2.)
- 9. Press Channel Setting, for the menu of channel settings.
  - 1) Press DataLine, and use the knob to choose "All". Press the softkey again to select "All".
  - 2) Press F2 and choose "Open" to select the channel. (Line in yellow denotes that it is selected, line in white is not.)

- 3) Press SCLK, and use the softkey or the knob to select "C1".
- 4) Press Phase, and use the knob to choose "90°". Press the softkey again to set the phase to be 90 degree.
- 5) Press F5 to go to the next page.
- 6) Press Digit-Volt, and use the knob to choose "Off". Press the softkey again to select "Off".
- 7) Press Analo-volt, and use the keyboard to input and "1.8" and choose the unit of "V" to set the analog voltage to 1.8V.
- 8) Press DONE to complete the channel settings.
- 10. Press the blinking button Arb. All modified parameters (denoted with "\*") will be implemented. The "\*" sign will disappear, and the configuration status will change from "UnLoad" to "Loaded".

After all the above steps are finished, the screen display will be shown as inin Figure 3-4.

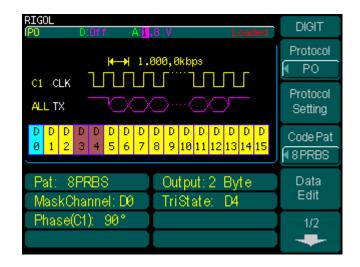


Figure 3-4 PO protocol output

# Example 5: To Generate an IIC Digital Waveform Using

### the PO Protocol

#### **Operation steps:**

1. Refer to Example 3 above, and use the default setup to output the IIC protocol timing sequence as shown in the diagram below:

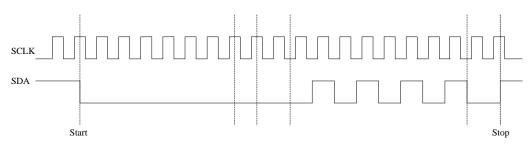


Figure 3-5 Time sequence of IIC protocol

From the above Figure 3-5, where SDA jumps from high to low when SCLK=1 indicates the "start" of the data output, and then; Address (0x00), Write operation (0), response bit (High Z), 1 Byte data (01 code pattern), response bit (High Z). The data output stops when SDA jumps from low to high when SCLK=1.

- In PO protocol, set "D0" as SCLK, "D1" as SDA and the code pattern as "User". Through editing the data in the user space, it can generate an IIC protocol digital waveform.
  - First the SCLK data, because SDA jumps when SCLK transits from high or low, SCLK data should not be set as 010101..., and it should be 001100110011...;
  - Since the IIC protocol has "high Z" output, the Mask Channel and the Tri-state Channel should be set; D1 is the Tri-state Channel for data output, and D2 is the Mask Channel.
  - 3) Data in D0 and D1 can be edited with reference to Figure 3-5. While D2 is the Mask Channel, setting the corresponding bits of D1 (Tri-state

Channel) to "1" will produce a High Z output; and the rest "0". Therefore, the binary data in the three channels should be shown as below:

- D0: 11 00 11

In the user space, each data has 16 bits (D15~D0). The first data bits of D2, D1, D0 are: 0, 1, 1, so the first data should be "0003", and the other data are shown below:

00000000	0003	0003	0002	0002	0003	0001
000000C	0000	0000	0001	0001	0000	0000
0000018	0001	0001	0000	0000	0001	0001
0000024	0004	0004	0005	0005	0000	0000
0000030	0001	0001	0000	0000	0001	0001
000003C	0000	0000	0001	0001	0000	0000
0000048	0001	0001	0000	0000	0001	0001
00000054	0000	0000	0001	0001	0000	0002
0000060	0003	0003	0002	0000	0001	0001
000006C	0000	0002	0003	0003	0002	0000
0000078	0001	0001	0000	0002	0003	0003
0000084	0002	0000	0001	0001	0000	0002
0000090	0003	0003	0002	0000	0001	0005
000009C	0004	0004	0005	0003	0002	0002

Judging from above, the total data length is 168 Bytes. The Generator will output the edited data the same way as the default configuration of IIC protocol. Users can adjust the output rate as needed.

# Chapter 4 Prompt Message & Troubleshooting

## **Prompt Message**

### Common Prompt Message

- Digital module is installed, Please wait, press help key to reset.
   Power-on prompting message 1. It prompts users that digital module has been connected.
- 2. Need Help? Press and hold down any key. Selected interface is USB. Mobile disk is moved. Not detect a digital module. Digital output will be closed.

Power-on prompting message 2. It prompts users that the setting has been restored; users can operate the instrument normally. When power-on, the system detects the current setting and USB interface has chosen; prompts not USB device is detected; and prompt that digital module has not connected.

3. Need Help? Press and hold down any key. Selected interface is USB. Mobile disk is moved. Detected a digital module.

Power-on prompting message 2. It prompts users that the setting has been restored; users can operate the instrument normally. When power-on, the system detects the current setting and USB interface has chosen; prompts not USB device is detected; and prompt that digital module has been connected.

4. The function is optional. Please connecting digital module to fulfill the performance. Getting more help, please visit www.rigol.com.

If digital module has not been connected, press | Utility  $| \rightarrow$  Output Setup  $\rightarrow$  Digit-Modu, when trying to choose "Power-On", this message will appear to prompt users digital module should be connected first.

# 5. Press "Arb" to complete change of configuration. Or switch to arbitrary wave mode if not change any parameter.

 Connect digital module, press
 Arb
 → Edit Digital W, this message will appear,

 © 2008 RIGOL Technologies, Inc.
 4-1

It prompts users to use Arb to finish the output.

6. Restore all instrument settings to factory defaults?

Press Utility  $\rightarrow$  System  $\rightarrow$  Set to Default; this message will appear.

### 7. All settings are restored to factory defaults.

Press Utility  $\rightarrow$  System  $\rightarrow$  Set to Default, all the data and settings will restore to default.

#### 8. Success to save file.

When succeeded to store the digital waveform; this message will appear.

9. Success to read file. Complete. Digital waveform data of user space have been changed.

When succeeded to recall the stored digital waveform; this message will appear.

# 10. The output of digital logic module is under some limit; please refer to the user Guide.

When choose "Independ" mode; this message will appear.

11. The output function of analog wave is disabled. The digital logic output range of speed and phase is extended. Please refer to the user Guide.

When choose "Depend" mode; this message will appear.

### 12. Wave file will cover\*\*!

When store a digital waveform in a location where a waveform file already exist, press Save; this message will appear.

### 13. Cover the file?

When store a digital waveform in a location where a waveform file already exist, press End/Store; this message will appear.

### 14. Delete the file?

Choose a file you want to delete, press Delete; this message will appear.

# 15. Just displays the trigger mode. It will be changed when burst mode is used.

Press Arb  $\rightarrow$  Edit Digital W $\rightarrow$  F5 $\rightarrow$  Trigger; this message will appear.

### 16. Digital logic module is triggered.

Press Burst  $\rightarrow$  Source  $\rightarrow$  "Manual", and press the button Trigger on the front panel of the instrument; this message will appear.

© 2008 RIGOL Technologies, Inc.

#### 17. Trigger Source of Digital Logic Signal has been changed to Manual.

Press Burst  $\rightarrow$  Source  $\rightarrow$  "Internal", press Trigger on the front panel of the instrument; this message will appear.

#### 18. Set the rate by bit per second (bps).

Press Arb  $\rightarrow$  Edit Digital W $\rightarrow$ RS232 $\rightarrow$ Baud, choose the "User" option; this message will appear.

#### 19. High level has been calibrated.

Press Arb  $\rightarrow$  Cal Digital W  $\rightarrow$  Calibration High volt, input the measured value, and press YES; this message will appear.

#### 20. Low level has been calibrated.

Press Arb  $\rightarrow$  Cal Digital W  $\rightarrow$  Calibration Low volt, input the measured value, and press YES; this message will appear.

#### 21. Please measure A0 channel, then input the value.

Press Arb  $\rightarrow$  Cal Digital W $\rightarrow$  Calibration High volt, (Calibration Low volt); this message will appear.

22. Voltage calibration coefficients are restored to factory defaults. Press  $Arb \rightarrow Cal Digital W \rightarrow Set to Default; this message will appear.$ 

#### 23. Please choose calibration item.

Press Arb  $\rightarrow$  Cal Digital W $\rightarrow$ Set to Default $\rightarrow$ YES; this message will appear.

#### 24. No changes.

When you are changing certain parameter, and press CANCEL; this message will appear to prompt users that there is not changed.

#### (\*\*denotes the content will vary according to the setting)

### Error Message

- Please select a valid File. You are trying reading analog wave into digital space; please confirm you are working under analog space.
   When users recall analog waveform at digital waveform editor interface; this message will appear.
- 2. Please select a valid File. You are trying reading digital wave into analog storage space, or please confirm you are working under the analog waveform edit it.

Press Store/Recall, enter the STORAGE menu, and choose a stored digital waveform to recall; this message will appear.

**3.** Invalid operation. Digital logic output module is under co-operation working with DG series.

If the current work mode is "Depend", analog waveform is disabled. Press any function key of analog wave; this message will appear.

#### 4. Invalid operation. Digital logic output module is power off.

If the digital module is power-off, pressing Utility  $\rightarrow$  Output Setup  $\rightarrow$  F5  $\rightarrow$ F3 (Work- mode); this message will appear.

5. Please power on the digital logic module
 If the digital module is power-off, press Arb, choose Edit Digital W and press
 Arb again; this message will appear.

#### 6. The address of 16 bits data must be even.

When edit the address, you can not select an odd address.

### Data Overflow

#### 1. Data length upper limit\*\* Bytes=\*\*k Bytes

RS232: Data length upper limit 35840 Bytes=35k Bytes SPI: Data length upper limit 40960 Bytes=40k Bytes IIC: Data length upper limit 10240 Bytes=10k Bytes PO: Data length upper limit 131072 Bytes=128k Bytes When user's setting exceeds limit, the system will automatically adjust it to the corresponding upper limit.

#### 2. Data length lower limit 1 Byte

The data length lower limit is 1 Byte for all the protocols. When user's setting is lower than 1 Byte, the system will automatically adjust it to be 1 Byte.

#### 3. \*\* Mbps

It denotes the transmission rate upper limit.

RS232: 60 Mbps

SPI: 60 Mbps

IIC: 30 Mbps

PO: 100Mbps

When user's setting exceeds limit, the system will automatically adjust it to the corresponding upper limit.

#### 4. 1bps

It denotes the transmission rate lower limit.

The transmission rate lower limit is 1 bps for all the protocols. When user's setting is lower than 1 bps, the system will automatically adjust it to be 1 bps.

#### 5. Offset Upper limit output overflow user space!

When the code pattern is "User", and the sum of "Offset" and the output length exceeds the upper limit of current protocol output length; this message will appear.

#### 6. Offset Lower limit 0

When the code pattern is "User" and the offset set by users is lower than 0; this message will appear.

## 7. Voltage upper limit \*\*V

Analog voltage:

RS232: 4.5V or -2.0V according to the type of the instrument. SPI、IIC、PO: 9.4V Digital voltage: RS232: Off SPI、IIC、PO: 4.2V When the set voltage exceeds the upper limit, the system will automatically adjust it to the corresponding upper limit.

#### 8. Voltage lower limit \*\*V

Analog voltage: RS232: 2.0V or -4.5V according to the instrument type. SPI、IIC、PO: 1.4V Digital voltage: RS232: Off SPI、IIC、PO: 1.4V. When the set voltage exceeds the lower limit, the system will automatically adjust it to the corresponding lower limit.

#### 9. Data Number per frame Upper limit 3 Byte

In SPI protocol setting, the upper limit of transmission data per frame is 3 Byte, if the specified value is higher than 3 Byte, the system will automatically adjust it to be 3 Byte.

#### 10. Data Number per frame Lower limit 1 Byte

In SPI protocol setting, the lower limit of transmission data per frame is 1 Byte, if the specified value is lower than 1 Byte, the system will automatically adjust it to be 1 Byte.

#### 11. IIC Address upper limit 127

In IIC protocol setting, when the user-defined address exceeds 127; this message will appear.

#### 12. IIC Address lower limit 0

In IIC protocol setting, when the user-defined address is lower than 0; this message will appear.

#### (\*\*denotes the content will vary according to the setting)

# Troubleshooting

- **1.** If digital module does not work after DG3000 powering up, please follow the steps below:
- (1) Turn off the instrument, check if the connection cable is correctly connected.
- (2) Turn on the instrument, check if the Digit-Modu option has set to be "Power-On".
- (3) If it does not work correctly, contact **RIGOL** for our service.
- 2. If all the settings are correct, but no waveforms output, please follow the steps below:
- (1) Check if the SMB-to-SMA RF coaxial line or logic analyzer testing lines you are using are correctly connected.
- (2) Set the Power On option to be Last when all the above steps have been finished.
- (3) Restart the instrument.

# Chapter 5 Specifications

All these specifications apply to the logic signal output module of DG3000 Series Function/Arbitrary Waveform Generator unless otherwise specified. To reach these specifications, two conditions must be met first:

- The instrument must have been operating continuously for more than 30 minutes within the specified operating temperature.
- Must perform the Test/Cal (Calibrate) operation if the operating temperature changes by more than 5 °C.

### Specifications

Public Specification			
Digital voltage range	1.4~4.2V (Typical and user-defined) (except RS232)		
Trigger mode	Auto, Burst mode trigger		
Code pattern	All"0"、All"1"、"01"、Pseudo-random code、User data		
User space	256k bytes online-edit, 2M bytes output (UltraWave)		
PO			
Analog voltage range	1.4~9.4V		
CLK	C0、C1		
Clock phase	0 <sup>°</sup> 、90 <sup>°</sup> 、180 <sup>°</sup> 、270 <sup>°</sup>		
Data line	D0~D15		
Transmission rate	1bps~100Mbps		
Data output range	1Byte~128k Bytes		
Mask Channel	None, D0~D15		
Tri-state Channel	None, D0~D15		
IIC			
Analog voltage range	1.4~9.4V		
Bit sequence	MSB		
CLK	D0~D15		
Data line	D0~D15		
Transmission rate	1bps~15Mbps		
Data output range	1Byte $\sim$ 10k Bytes		
Optional output address	None、7bits address		
Operation	Write, Read		
SPI			
Analog voltage range	1.4~9.4V		
Bit sequence	MSB		
CLK	D0~D15		
Data line (SDA)	D0~D15		
TFS	D0~D15		
CS	D0~D15		
Transmission rate	1bps~60Mbps		
Data output range	1byte $\sim$ 40k bytes		
SPI type	SPI2, SPI3		
Clock polarity	0、1		
Clock phase	0、1		
CS level	High level、Low level		
#Data	1~3Bytes		
TFS	FS_H、FS_L、LD_H、LD_L or NONE		
RS232	·		
Analog voltage range	2.0V~4.5V -4.5V~-2.0V		
Bit sequence	LSB		

Data line (TX)	D0~D15	
Transmission rate	$1Bps{\sim}60MBps$	
Data output range	1Byte $\sim$ 35k Bytes	
#Data	5~8bits	
verification mode	Supports None、Odd、Even、Fixed0 and Fixed1.	
#StopBit	Supports 1bit, 1.5bit and 2bits	

# Chapter 6 Appendix

# **Appendix A: Accessories**

# DG3000 Logic signal Output Module provides the following standard accessories:

- A connection cable between DG3000 and the logic signal output module
- 9 SMB-to-SMA RF coaxial lines
- 20 logic analyzer testing lines
- 20 logic analyzer testing clips
- A User's Guide

## **Appendix B: Warranty**

**RIGOL** Technologies, Inc. warrants its products' mainframe and accessories in materials and workmanship within the warranty period. Within the warranty period, **RIGOL** guarantees free replacement or repair of products which are proven defective.

To get repair service or obtain a copy of the whole warranty statement, please contact with your nearest **RIGOL** sales and service office.

**RIGOL** does not provide any other warranty items except the one being provided by this summary and the warranty statement. The warranty items include but not being subjected to the hint guarantee items related to tradable characteristic and any particular purpose.

**RIGOL** will not take any responsibility for cases of indirect damages, improper use and ensuing damages.

# **Appendix C: Maintenance**

#### **General Care**

Do not store or leave the instrument in the place where the LCD display will be exposed to direct sunlight for long periods of time.



**CAUTION:** To avoid damage to the instrument, do not expose them to sprays, liquids, or solvents.

#### Cleaning

Check the Instrument frequently according to the operation conditions; follow the steps below to clean the exterior surface of the instrument:

- Remove loose dust on the outside of the instrument with a lint- free cloth. Take care to avoid scratching the clear plastic display filter.
- Please disconnect the power. Use a soft cloth dampened with water to clean the instrument. To avoid damage to the surface of the instrument, do not use any abrasive or chemical cleaning agent.



**WARNING:** Before restarting the instrument, make sure that the instrument is dry to avoid any short-circuit or body damage because of the moisture.

## **Appendix D: Contact RIGOL**

If you have any problem or requirement occurs when using our products, please contact **RIGOL** Technologies, Inc. or the local distributors.

Domestic: Please call Tel: (86-10) 8070 6688 Fax: (86-10) 8070 5070 Service & Support Hotline: **800 810 0002** 9:00 am -5: 00 pm from Monday to Friday

> Or by e-mail: service@rigol.com

Or mail to: **RIGOL** Technologies, Inc. 156# CaiHe Village, ShaHe Town, ChangPing District, Beijing, China Post Code: 102206

Overseas: Contact the local **RIGOL** distributors or sales office.

For the latest product information and service, visit our website: <u>www.rigolna.com</u>

# Index

*	1-9
#Data	2-4
#StopBit	2-4
01	1-10, 2-12
16PRBS	1-10
32PRBS	1-10
8PRBS	1-10
Address	1-12, 2-8
All0	
All1	2-12
Analo-volt	
Baud rate	1-10, 2-28
Browser	
Burst mode	
Chinese input	2-20
Cleaning	6-3
Clock Pha	2-6
Clock Pol	
CS Level	
DataPage	
DataSource	1-12, 2-16
Depend	
English input	
External trigger	2-33
File Type	
IIC	1-10
Independ	1-6

Internal trigger	2-31
	1-9
Manual trigger	2-34
	2-10
Operation	2-8, 2-42
Output Length	2-27
Phase	2-42
РО	1-10
Ports	1-2, 1-4
Power-Off	1-6
Power-On	1-6
Protocol	1-9, 1-10
Rate	1-10, 2-30
Recall	1-12, 2-18
RS232	1-10
Save	1-12, 2-18
	2-38, 2-40, 2-42
SDA	
Specifications	5-2
SPI	1-10, 2-6, 2-38
SPI3	3-4
TFS	2-6, 2-38, 2-40
Unload	1-9
	1-8, 1-10, 2-8
Verify	2-4
volatile memory	1-8, 2-16
ZStatus	2-10

1